

FIG. 1A

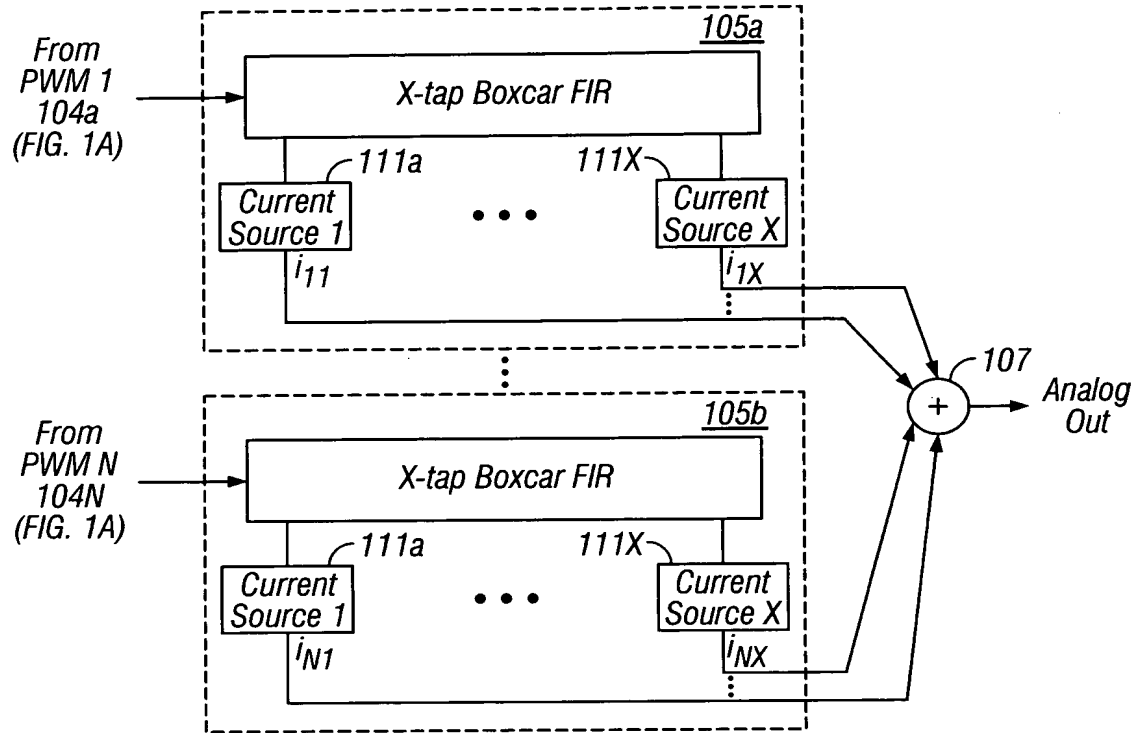


FIG. 1B

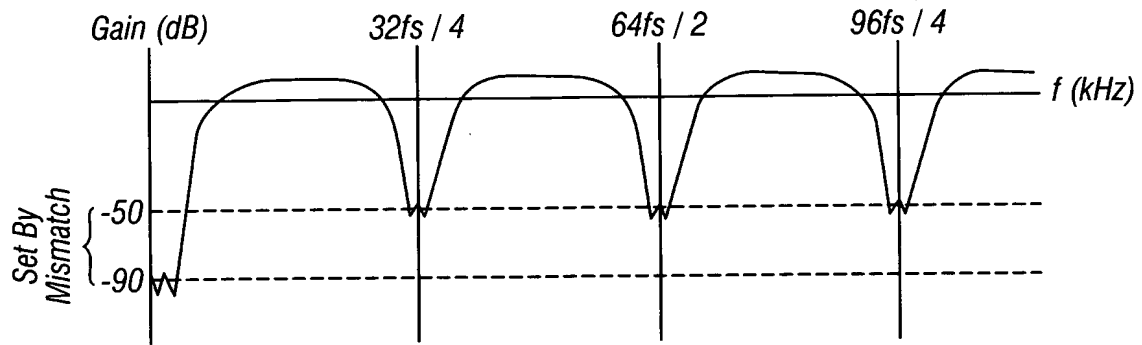


FIG. 2A

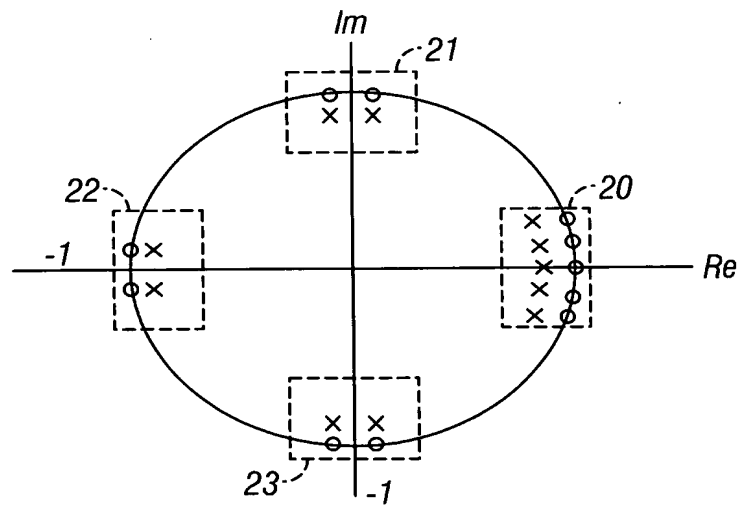


FIG. 2B

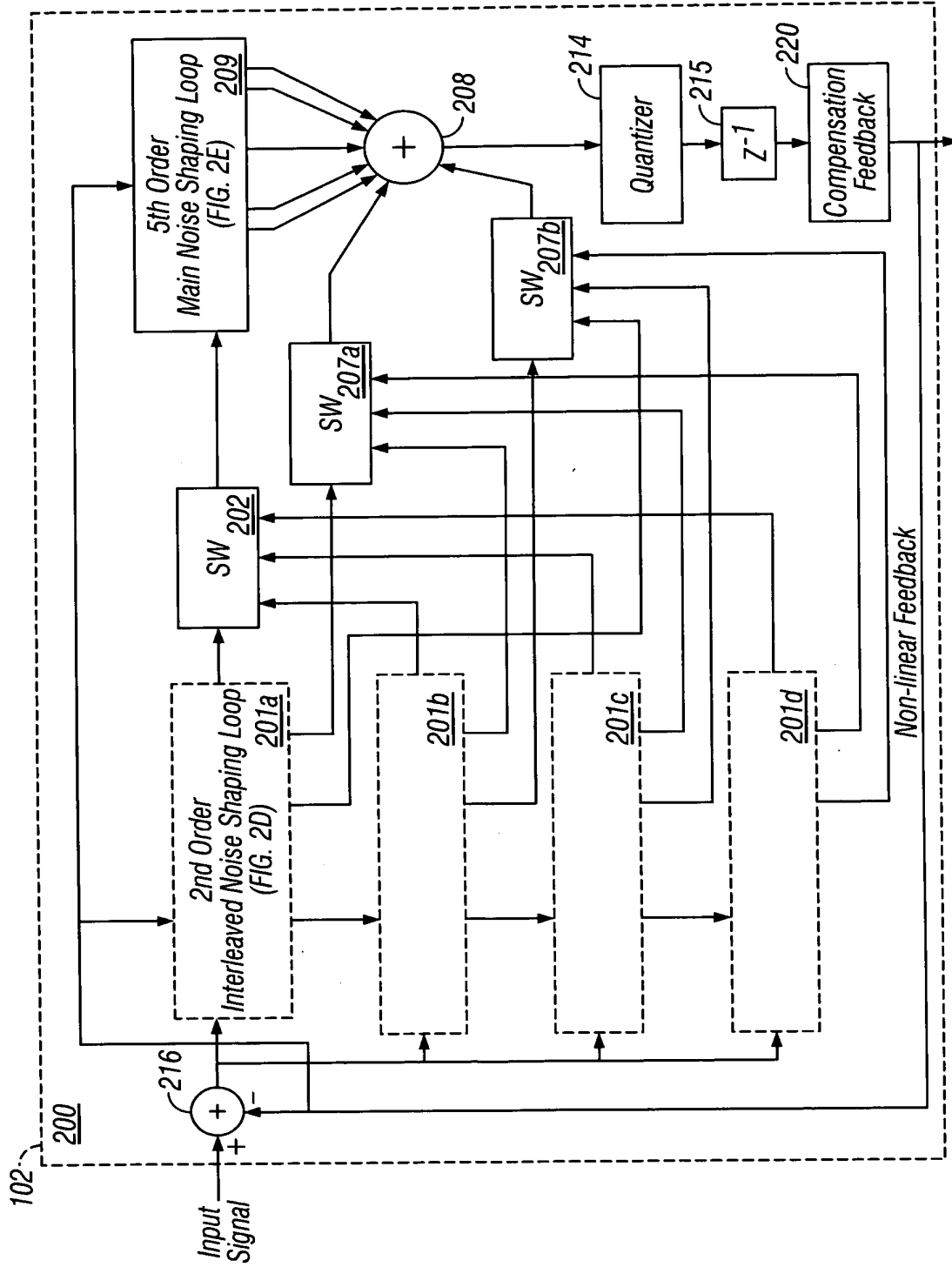


FIG. 2C

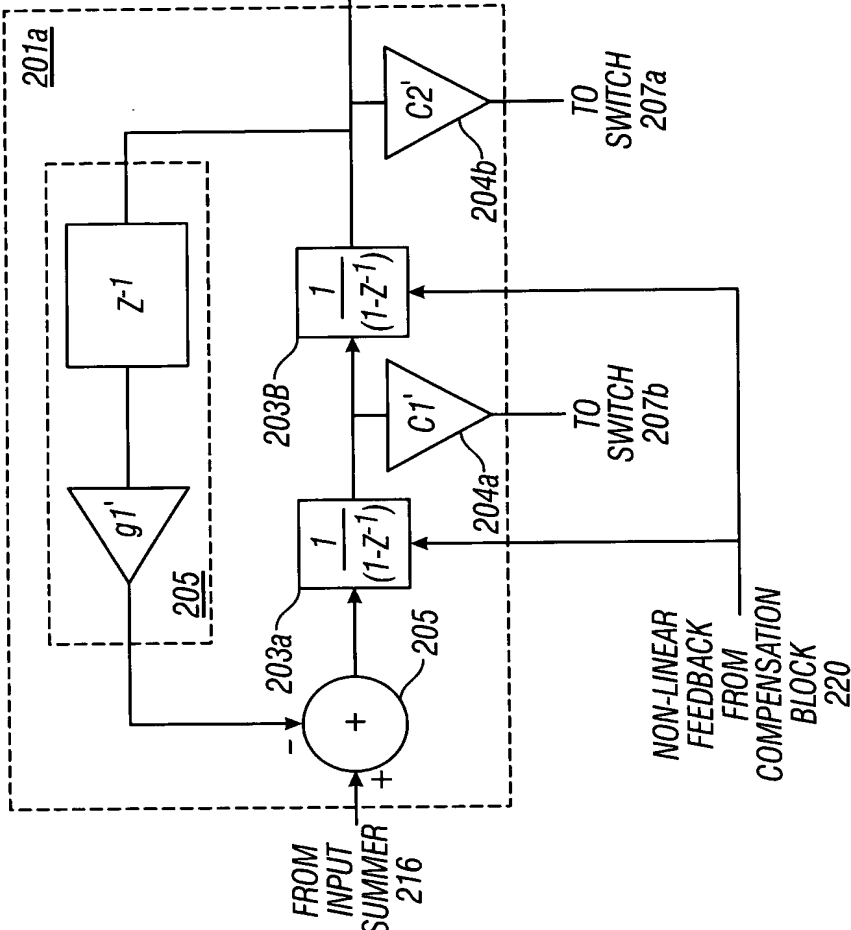


FIG. 2D

FIG. 2E

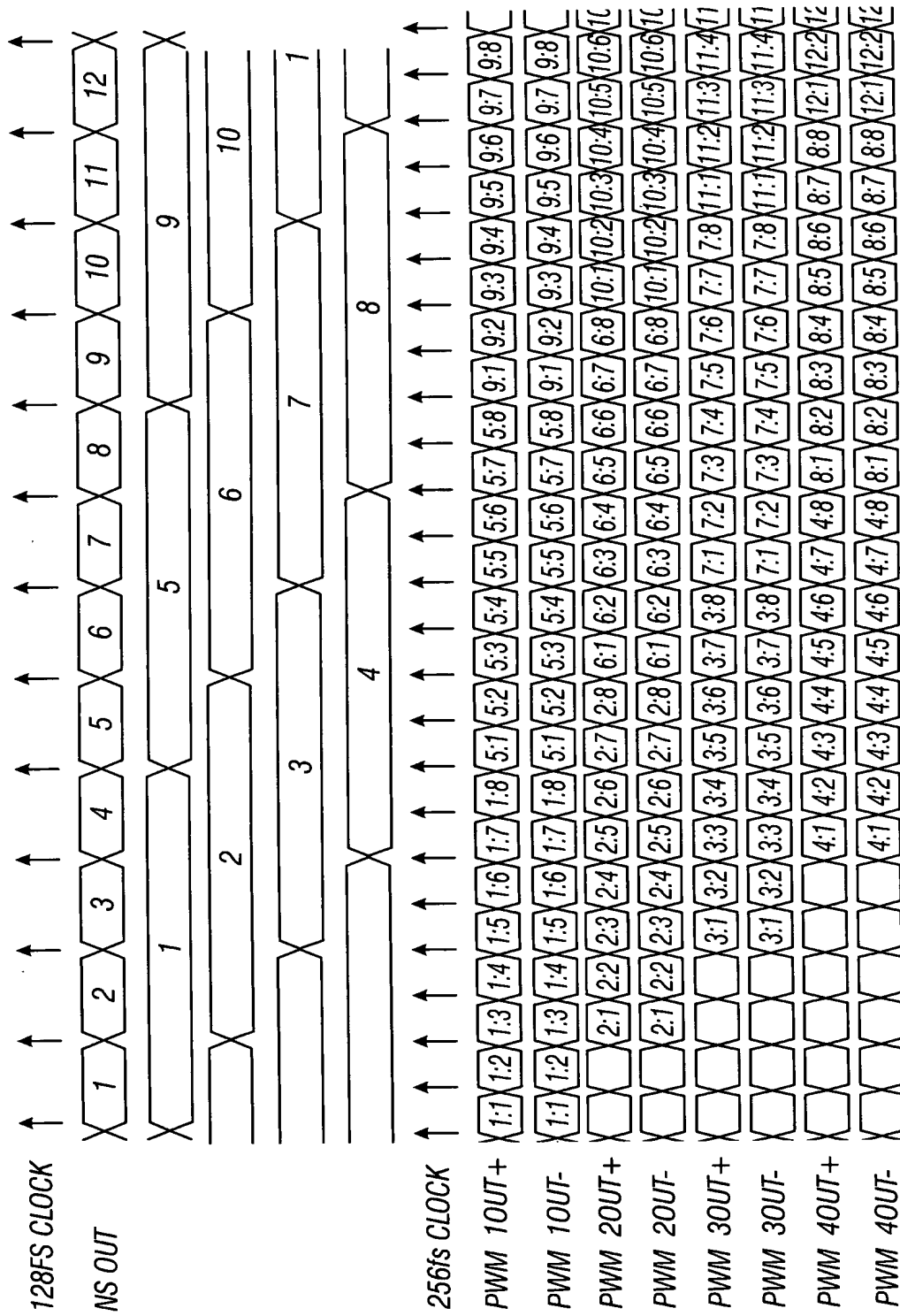


FIG. 3

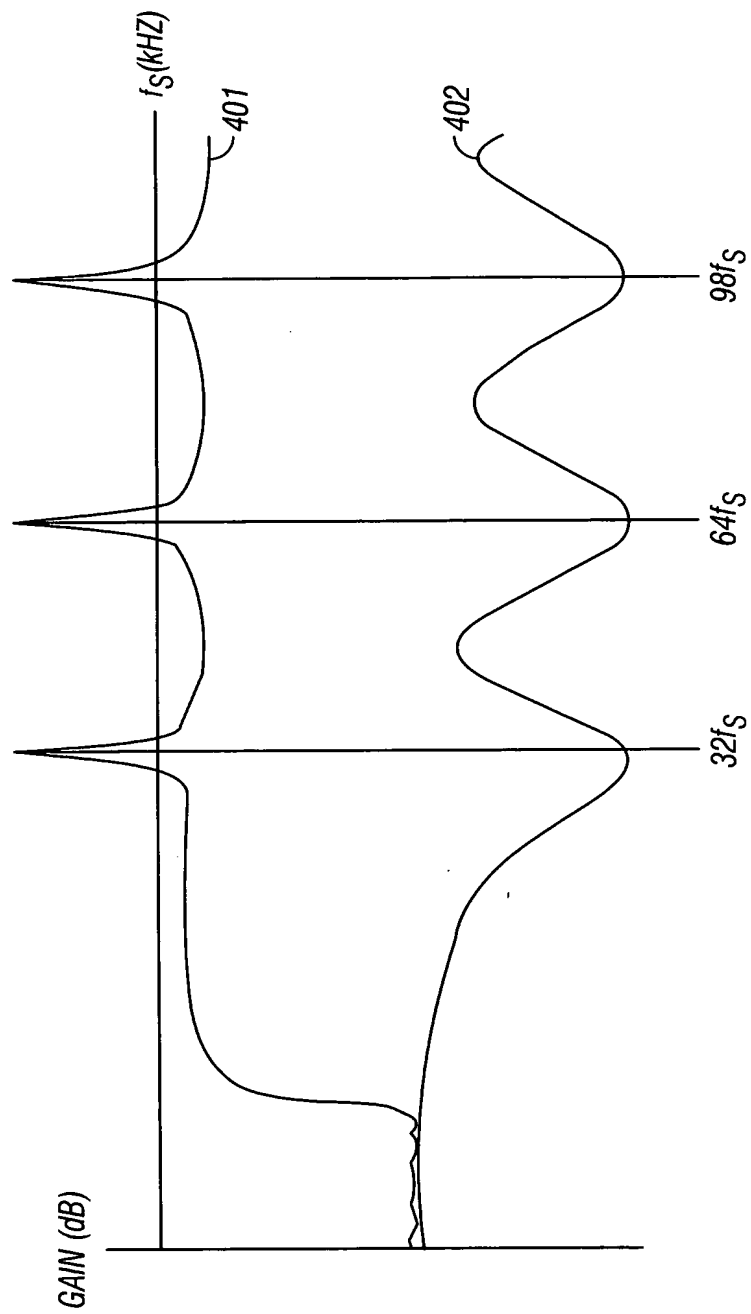


FIG. 4



FIG. 5A



FIG. 5B



FIG. 5C

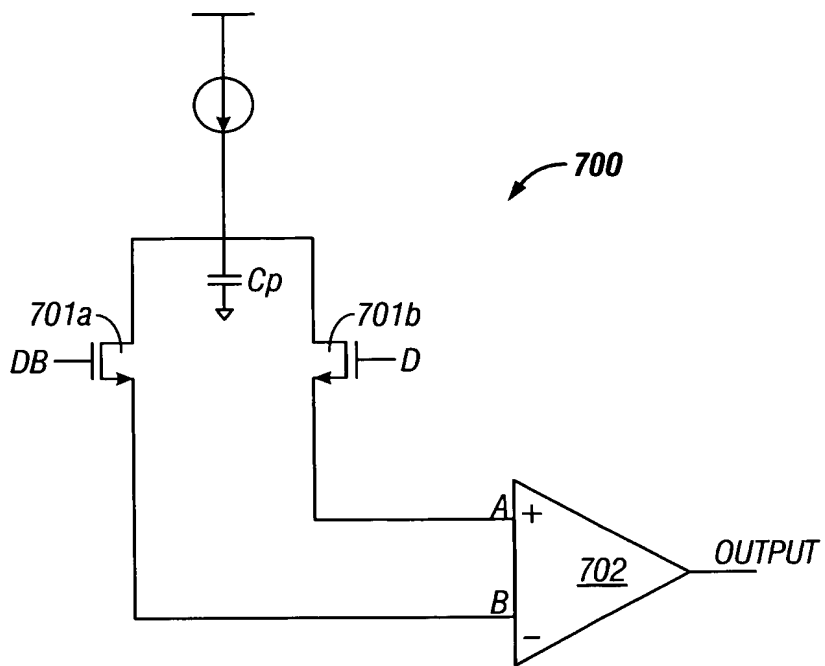
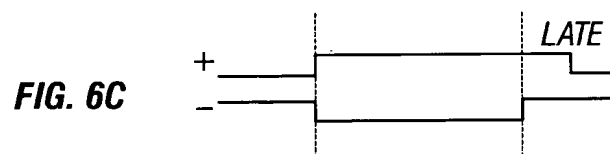
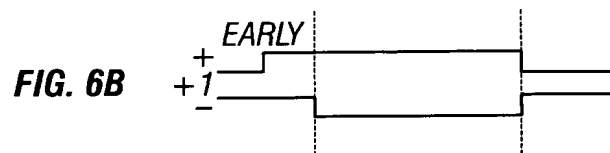
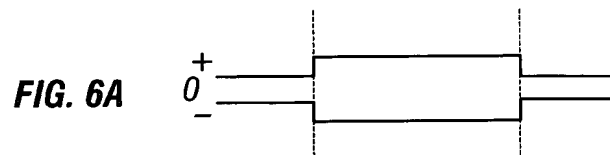


FIG. 7A

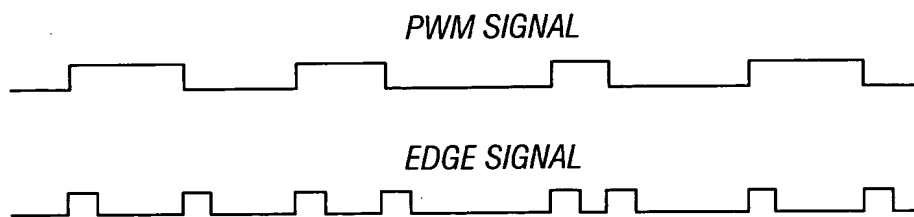


FIG. 7B

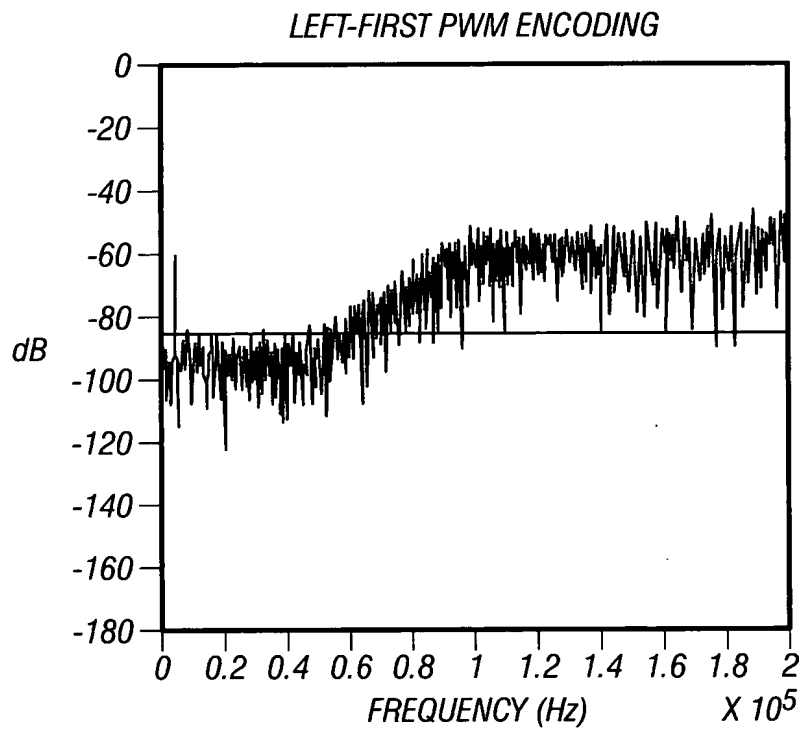


FIG. 7C

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RIGHT-FIRST PWM ENCODING

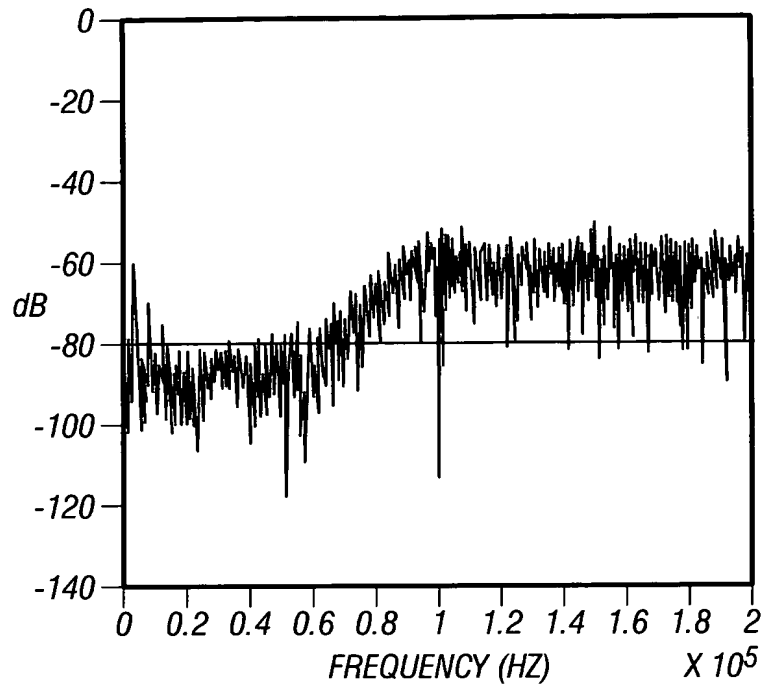


FIG. 7D

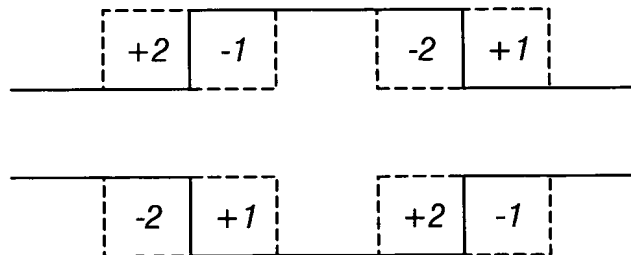


FIG. 8A

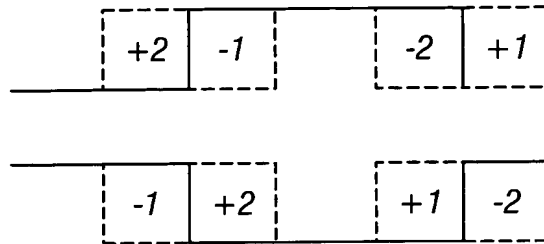
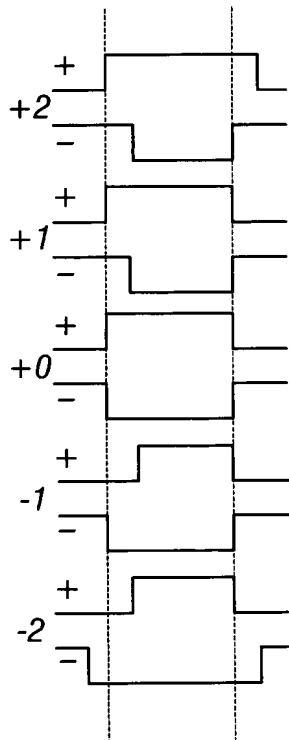


FIG. 8B

PWM PULSES W/SHAPER=0



EDGE SIGNAL WITH SHAPER=0

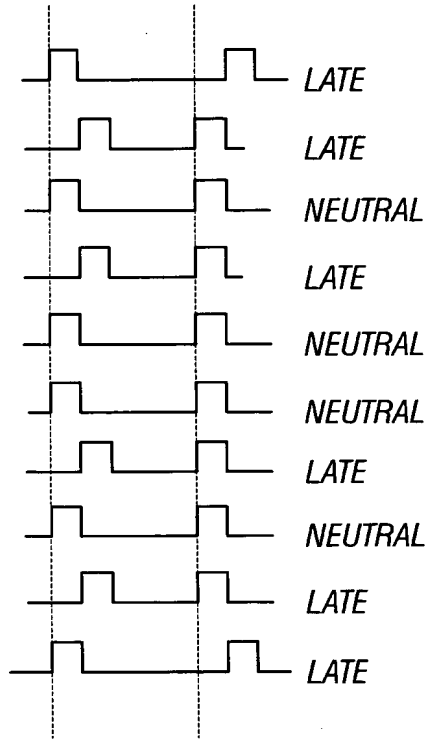
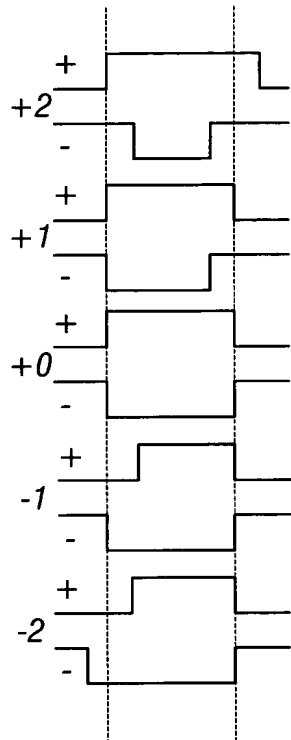


FIG. 9A

PWM PULSES W/SHAPER=0



EDGE SIGNAL WITH SHAPER=0

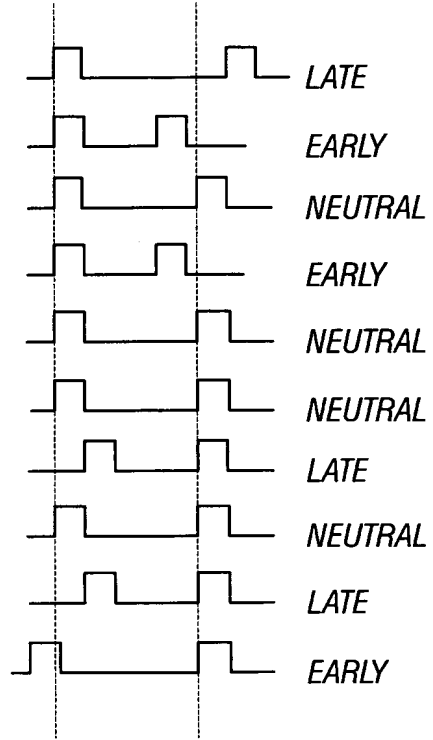


FIG. 9B

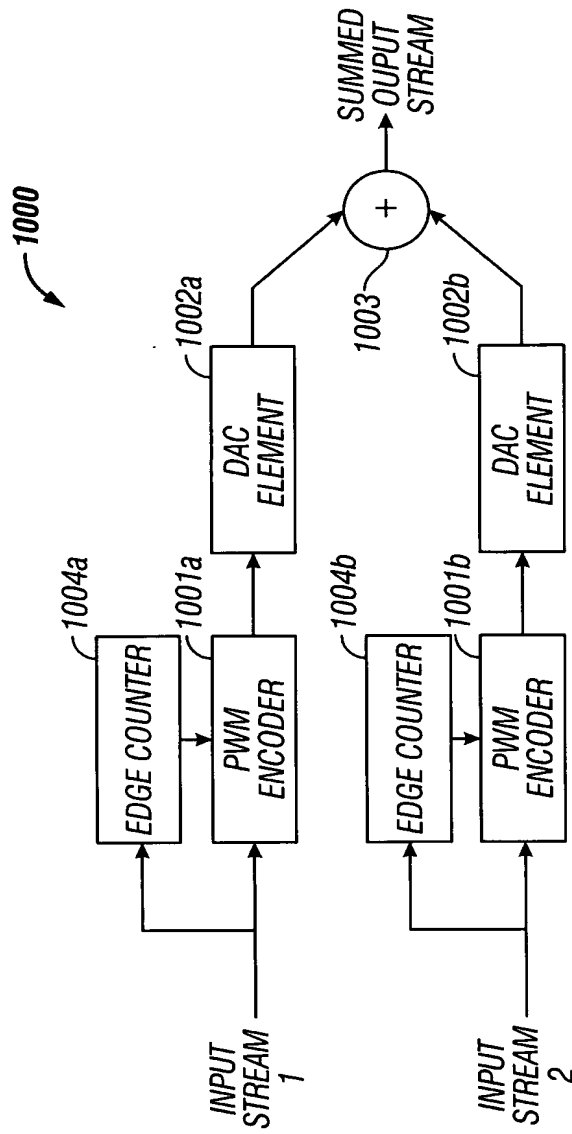


FIG. 10

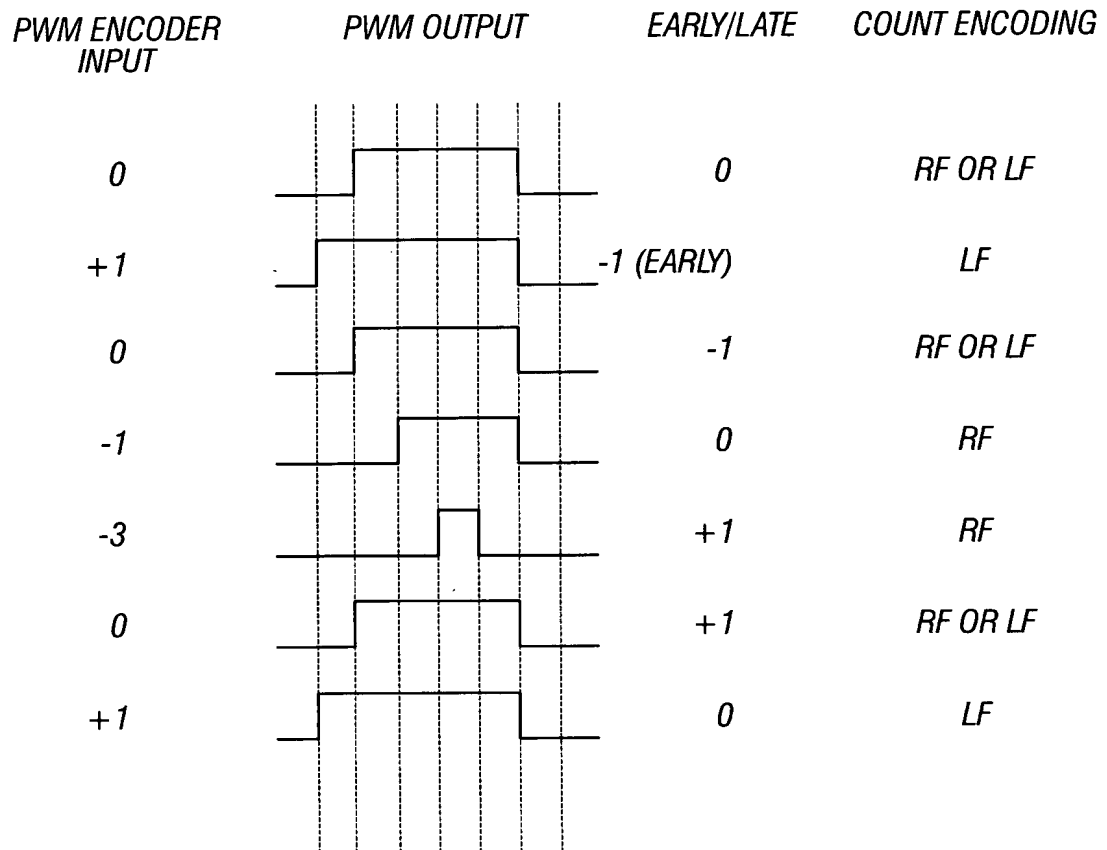


FIG. 11

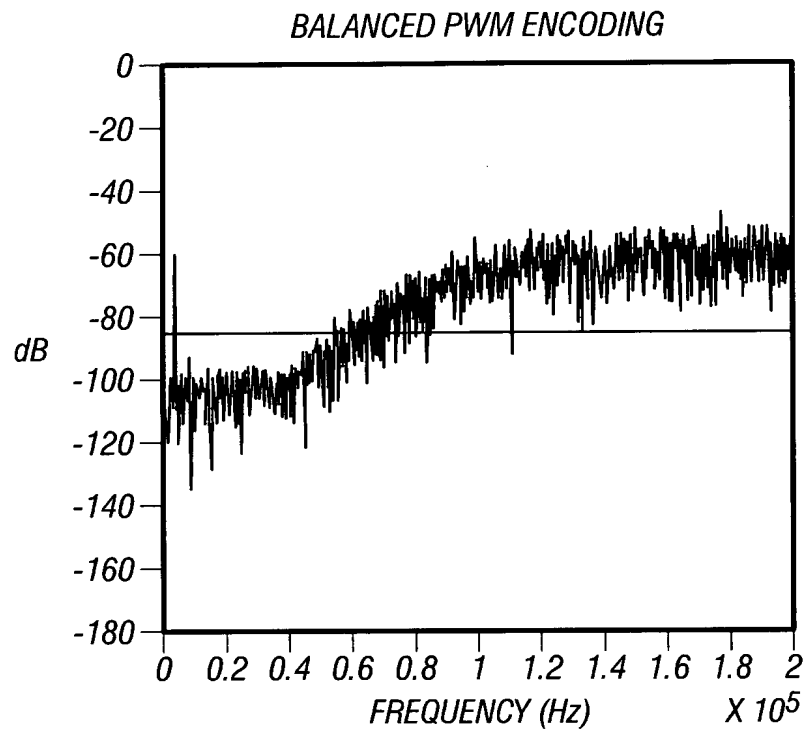


FIG. 12